



ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /MCK/

Substitution of form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)	Complete if Known	
	<b>Application Number</b>	10/676,478
	<b>Filing Date</b>	September 30, 2003
	<b>First Named Inventor</b>	Adi-Tabatabai, Ali-Reza
	<b>Group Art Unit</b>	2186
	<b>Examiner Name</b>	Krofcheck, Michael
Sheet 1 of 1	Attorney Docket No: 42P17411	

US PATENT DOCUMENTS					Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
Examiner Initial *	Cite No	USP Document Number	Publication or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of cited Document	
		US-6,879,266	04/12/2005	Dye, Thomas A., et al.	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
		"10/676,430 Final Office Action", (4/15/2008), 19 pages		T*
		"10/676,430 Non-Final Office Action", (11/27/07), 19 pages		
		ABALI, BULENT, et al., "Hardware Compressed Main Memory: Operating System Support and Performance Evaluation", <u>IEEECS Log Number 114250</u> , (2001), 11 pages		
		ABALI, B., et al., "Memory Expansion Technology (MXT): Software support and performance", <u>International Business Machines Corporation</u> , (2001), 15 pages		
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		ALAMELDEEN, ALAA R., et al., "Adaptive Cache Compression for High-Performance Processors", <u>Proceedings of the 31st Annual International Symposium on Computer Architecture (ISCA-31)</u> , Munich, Germany, (June 19-23, 2004), 12 pages		
		FRANASZEK, P. A., et al., "Algorithms and data structures for compressed-memory machines", <u>International Business Machines Corporation</u> , (2001), 14 pages		
		HALLNOR, ERIK G., et al., "A Compressed Memory Hierarchy using an Indirect Index Cache", <u>Advanced Computer Architecture Laboratory, EECS Department, University of Michigan</u> , Ann Arbor, MI, (2004), 17 pages		

EXAMINER

/Michael Krofcheck/

DATE CONSIDERED

07/16/2008

Based on PTO/SB06A/06-05 - Substitute Disclosure Statement Form (PTO-1449) as modified by ISITZ 03/09/07  
 \* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 909. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. Applicant's unique citation designation number (optional) 2 Applicant to place a check mark here if English language Translation is attached